

## **PATENT**

## In re Application of:

James M. Derderian

Serial No.: 09/938,106

Filed: August 23, 2001

For: ASSEMBLIES INCLUDING STACKED SEMICONDUCTOR DEVICES SEPARATED BY DISCRETE CONDUCTIVE ELEMENTS THEREBETWEEN, PACKAGES INCLUDING THE ASSEMBLIES, AND METHODS

Examiner: Unknown

Group Art Unit: 2811

**Attorney Docket No.:** 4832US (01-0104)

## CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail (under 37 C.F.R. § 1.8(a)) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

October 26, 2001 Date of Deposit

Signature of positive of practitioner or othe person having reasonable basis to expect mailing to occur on date of depositishown pursuant to 37 C.F.R. § 1.8(a)(1)(ii)

Deidra Pfeil
Typed/printed name of person whose signature is contained above

## TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents Washington, D.C. 20231

Sir:

Attached please find formal drawings for the above-referenced application.

Respectfully submitted,

Brick G. Power

Registration No. 38,581

Attorney for Applicant

TRASK BRITT

P. O. Box 2550

Salt Lake City, Utah 84110-2550

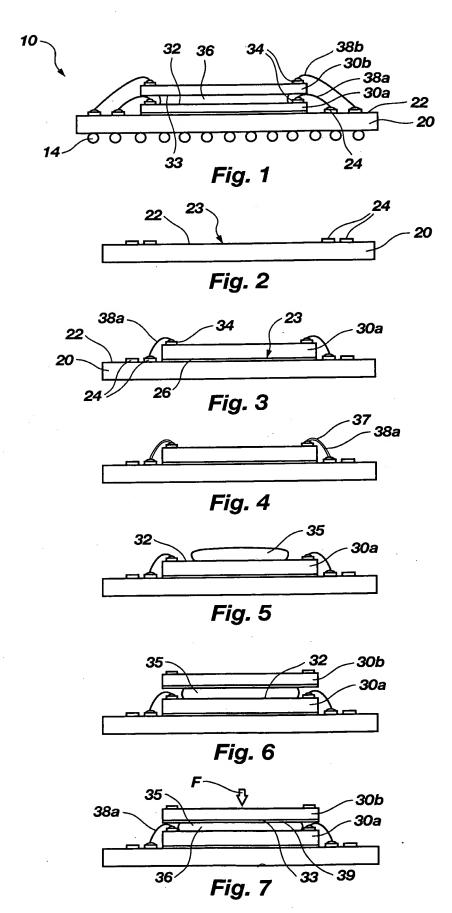
Telephone: (801) 532-1922

Date: October 26, 2001

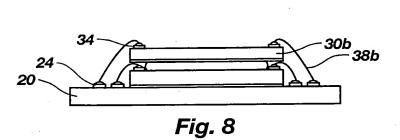
Enclosure: Formal Drawings (3 sheets)

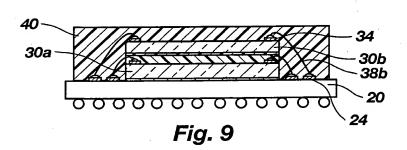
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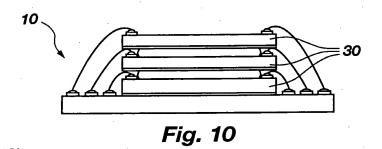




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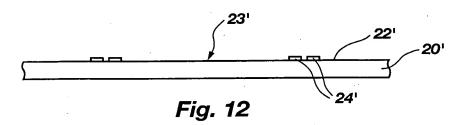


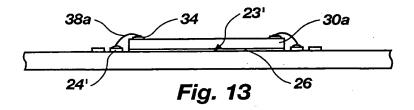


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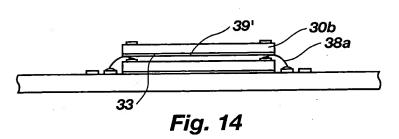
Fig. 11

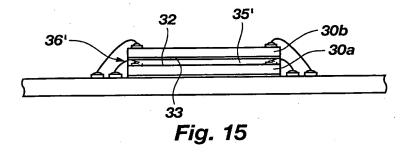
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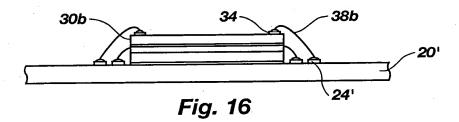




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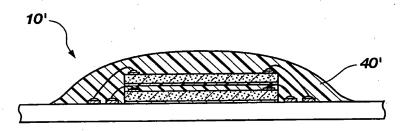


Fig. 17

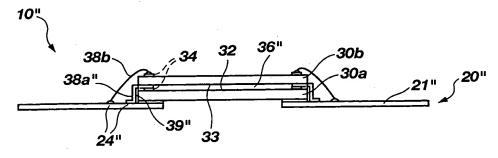


Fig. 18

